

This Non Disclosure Agreement (NDA) is made on 15/ 06/ 2017 between Semi-Conductor Laboratory, Department of Space, Government of India, Sector 72, SAS Nagar – 160 071, Punjab (hereinafter called "SCL") and Vignan's Foundation for Science, Technology and Research University, Vadiamudi – 522 213, Guntur, AP (hereinafter called VFSTRU).

- VFSTRU is desirous of designing some CMOS circuits for fabrication in SCL Wafer Fab.
- 2. SCL will be furnishing to VFSTRU SCL's 0.18 Micron CMOS Process Design Kit (PDK) and other details required for the sole purpose of designing the CMOS circuits. VFSTRU does not have the rights of porting the process and of manufacturing products based on furnished information and can not transfer the furnished information to any third party without the written permission of SCL.
- 3. All information furnished by SCL shall be treated by VFSTRU as Confidential and shall not be used by VFSTRU except for the above said use and shall not be disclosed by VFSTRU to any third party without SCL's prior written consent, except for any of the information which VFSTRU can show that is:
 - already known to VFSTRU on the date it was disclosed to it by SCL and is or becomes free of restriction on the disclosure or use in question, or
 - generally known or freely available to the public (except by reason of any breach by VFSTRU of its obligations hereunder), or
 - disclosed to VFSTRU free of restriction on the disclosure or use in question, by
 a third party who was entitled to make such unrestricted disclosure.
- 4. VFSTRU will take such precautions and make such arrangements as are reasonably necessary to protect the information received by it (and in any event no less than those VFSTRU would take and make to protect its own confidential information). VFSTRU will inform such of its employees/students/research scholars as are concerned in it's above said use of the confidential nature of the information received, and will prohibit them from taking copies of any of it.

ग्रुप प्रमुख-परियोजना नियोजन ग्रुप Group Head-Project Planning Group संमी-कंडक्टर लेबोरेटरी Semi-Conductor Laboratory

अंतरिक्ष विभाग, भारत सरकार Department of Space, Government of India

- VFSTRU obligations under the above clauses will continue in respect of the information for a period of 10 years after its disclosure to them.
- VFSTRU will return to SCL all copies of the SCL documents received by them in connection with this agreement upon SCL's request.

The parties hereto have caused the agreement to be signed, in duplicate, as of the day and year first written above. One copy each of the signed agreement is being retained by the signatories.

On behalf of

Semi-Conductor Laboratory
Department of Space
Government of India
Sector – 72
SAS Nagar – 160 071
Punjab

On behalf of

Vignan's Foundation for Science Technology and Research University Vadlamudi – 522 213 Guntur District Andhra Pradesh

Signature:.... (With Date)

> संजय भटनागर SANIAY BHATNAGAR सुर क्ष्मुख-परियोजना नियोजन सुप Group Head-Project Planning Group

सेनी बोडक्टर होबोरेटरी Semi-Conductor Laboratory जातिक विभाग, भारत सरकार

 Signature: 16/6/2017

Name: Dr.V. Madhusudhan Rao

VIGNAN'S FOUNDATION

FOR SCIENCE, TECHNOLOGY AND RESEARCH (Declared to be Deemed University U/S 3 of UGC Act 1956)

VADLAMUDI-522 213. A.P. INDIA

Designation:

Designation: Vice-Chancellor Incharge

Witness

Witness

Signature

Signature

Name

Name

: Dr. Avireni Srinivasulu

Designation

Designation

: Professor Dept of ECE

VADLAMUDI

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